



Gennum Launches GS4915 ClockCleaner™ IC

Burlington, Ontario September 5, 2006 – Gennum Corporation (TSX: GND), the leader in integrated circuits for professional video equipment, today announced the launch of the GS4915 ClockCleaner™ IC. The GS4915 reduces the jitter on the most common clock frequencies for SD-SDI, HD-SDI and 3G-SDI to below 20ps peak-to-peak for the important 100kHz to 10MHz frequency band. Gennum will be demonstrating the device at the International Broadcasters Convention (Booth 10.115) September 8 – 12 in Amsterdam.

Multi-Rate Jitter Reduction

“The GS4915 addresses one of the most common challenges for the video board designer – jitter,” noted Michael Alford, Manager of Video Transport Product Marketing at Gennum. The GS4915 cleans clocks of 27MHz, 74.25/74.18MHz or 148.5/148.35MHz. Other clock frequencies are automatically bypassed through the device. Traditionally system designers have relied on discrete VCXO-based circuits to reduce the jitter of clocks, with one VCXO for each possible clock frequency. For example, a product that supports 27MHz, 74.25MHz and 74.18MHz would require a discrete circuit involving three VCXOs. All this can now be replaced by the 6mm x 6mm GS4915 and the 3.2mm x 4.5mm GO1555 VCO from Gennum.

Important for 3G-SDI

“The GS4915’s superior jitter performance is important because it enables SDI at 3Gb/s” added Alford. The SMPTE 424M 3G-SDI standard allows only 101ps of alignment jitter at the SDI serial output. This number must be achieved with some margin after the serializer and cable driver. An ultra-low jitter parallel clock such as the one provided by GS4915 is critical in achieving this required system jitter performance.

Flexible and Versatile

The GS4915 has a number of features that make it easy to use in many different applications:

- Optional frequency double pin from 74.25/74.18MHz to 148.5/148.35MHz allows designers to deliver a /10 clock to an HD serializer instead of the /20 clock, which can allow for better serializer jitter performance.
- An optional output skew control pin provides a convenient way to adjust for downstream set-up and hold requirements.
- The user can choose between either single-ended or differential inputs. Both single-ended and differential outputs are provided.

Complement to GS4911B Genlock Device

While the GS4915 has been designed to operate as a general purpose clock cleaner, it has also been designed to complement the GS4911B genlock device. The GS4911B generates video clocks locked to an HVF reference, such as is needed when locking an SDI output to a video reference signal. The GS4911B has a programmable loop bandwidth in the Hz range and therefore filters out low-frequency

jitter on the H input. With a loop bandwidth near 50kHz, the GS4915 complements the GS4911B by filtering out higher-frequency jitter.

The GS4915 will be available for sale in November 2006.

About Gennum Corporation

Gennum Corporation designs, manufactures and markets industry-leading semiconductors and semiconductor-based products for leading segments of the global video, audio, and data communications markets. The Company's understanding of the unique needs of its targeted markets allows it to deliver high performance solutions that provide superior value. Gennum serves an international customer base from its head office in Burlington, Canada, and subsidiaries in Japan and the United Kingdom. The Company has design centres in Burlington and Ottawa, Canada, and in the United Kingdom.

For more information, please visit www.gennum.com.

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